



AV600-RH-AB

Military IP65 GPU Computer

Intel 13th Raptor Lake-H i7-13800HRE Processor,

Nvidia MXM A2000 GPU



User's Manual

Revision Date: Mar.13 2025

Safety Information

Electrical safety

- To prevent electrical shock hazard, disconnect the power cable from the electrical outlet before relocating the system.
- When adding or removing devices to or from the system, ensure that the power cables for the devices are unplugged before the signal cables are connected. If possible, disconnect all power cables from the existing system before you add a device.
- Before connecting or removing signal cables from the motherboard, ensure that all power cables are unplugged.
- Seek professional assistance before using an adapter or extension cord. These devices could interrupt the grounding circuit.
- Make sure that your power supply is set to the correct voltage in your area.
- If you are not sure about the voltage of the electrical outlet you are using, contact your local power company.
- If the power supply is broken, do not try to fix it by yourself. Contact a qualified service technician or your local distributor.

Operation safety

- Before installing the motherboard and adding devices on it, carefully read all the manuals that came with the package.
- Before using the product, make sure all cables are correctly connected and the power cables are not damaged. If you detect any damage, contact your dealer immediately.
- To avoid short circuits, keep paper clips, screws, and staples away from connectors, slots, sockets and circuitry.
- Avoid dust, humidity, and temperature extremes. Do not place the product in any area where it may become wet.
- Place the product on a stable surface.
- If you encounter any technical problems with the product, contact your local distributor

Statement

- All rights reserved. No part of this publication may be reproduced in any form or by any means, without prior written permission from the publisher.
- All trademarks are the properties of the respective owners.
- All product specifications are subject to change without prior notice

AV600-RH-AB User's Manual

Revision Date: Mar. 13. 2025

Revision History

Revision	Date (yyyy/mm/dd)	Changes
Version 1.0	2025/03/13	Initial release

Packing list

- ▶ AV600-RH-AB Military IP65 GPU Rugged System
- ▶ CD (Driver + User manual)

If any of the above items is damaged or missing, please contact your local distributor.



Index

CHAPTER 1: PRODUCT INTRODUCTION5

1-1. KEY FEATURES5

1-2. DIMENSIONS(2D)6

1-3. PANEL COMPONENT7

CHAPTER 2: JUMPERS AND CONNECTORS LOCATIONS8

2.1. D38999 CONNECT PIN DEFINITIONS.....8

CHAPTER 3: BIOS SETUP ITEMS11

3.1 INTRODUCTION11

3.2 BIOS SETUP11

3.2.1 MAIN12

3.2.2 CONFIGURATION12

3.2.2.1 CPU CONFIGURATION13

3.2.2.2 CHIPSET CONFIGURATION14

3.2.2.3 GRAPHICS CONFIGURATION15

3.2.2.4 eDP-TO-LVDS CONFIGURATION.....15

3.2.2.5 OEM PROFILE.....16

3.2.2.6 POWER CONTROL CONFIGURATION.....17

3.2.2.7 PCI/PCIE CONFIGURATION17

3.2.2.8 COME PEG PORT 0, 8, 1218

3.2.2.9 PCH PCI EXPRESS ROOT PORT 1~518

3.2.2.10 LAN CONFIGURATION19

3.2.2.11 SATA CONFIGURATION20

3.2.2.12 SATA AND RST CONFIGURATION20

3.2.2.13 USB CONFIGURATION21

3.2.2.14 TPM CONFIGURATIN22

3.2.2.15 SUPER IO CONFIGURATION23

3.2.2.16 SERIAL PORT 1 CONFIGURATION23

3.2.2.17 SERIAL PORT 2 CONFIGURATION24

3.2.2.18 H/W MONITOR24

3.2.2.19 SERIAL PORT CONSOLE REDIRECTION.....25

3.2.2.20 COM0 CONSOLE REDIRECTION SETTINGS26

3.2.2.21 EC FIRMWARE UPDATE.....27

3.2.3 SECURITY.....28

AV600-RH-AB User's Manual

Revision Date: Mar. 13. 2025

3.2.4	BOOT	29
3.2.5	SAVE & EXIT	30
3.3	BIOS / EC UPDATE	31
3.3.1	BIOS UPDATE	31
3.3.2	EC UPDATE	33

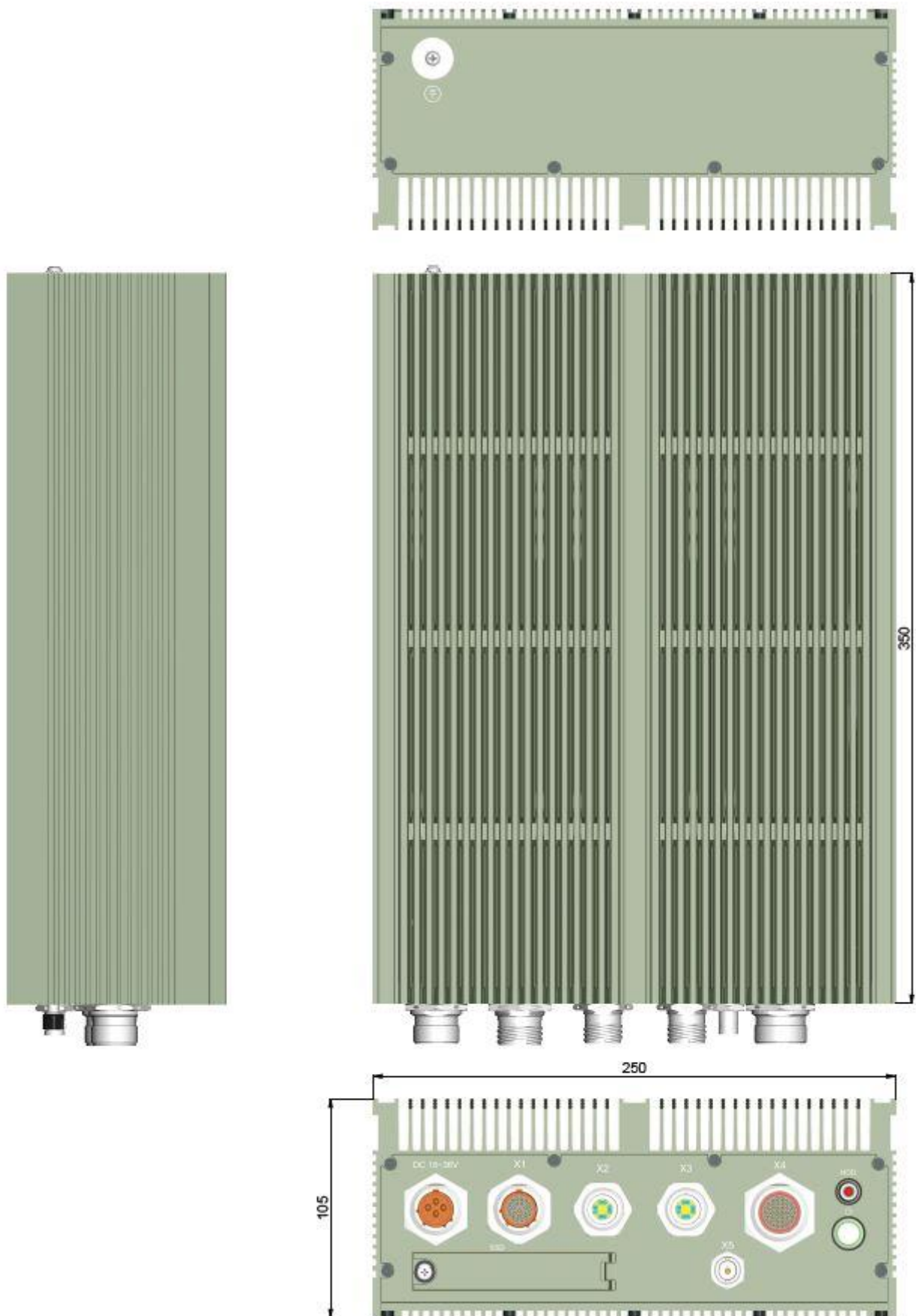
CHAPTER 1: PRODUCT INTRODUCTION

1-1. Key Features

System	
CPU	Intel® 13 th Raptor Lake-H i7-13800HRE,14C/20T, 2.5/5.0GHz, 24MB cache, 45W
Memory Type	16GB DDR5 5600MHz SO-DIMM (up to 96GB), non-ECC
GPU	NVidia® RTX A2000 2560 CUDA® cores, 4GB GDDR6
Expansion slot	2x Full-size mini PCIe (with SIM card slot)
	1x M.2 2280 M key (both PCIe x4 from PEG)
	1x SATAIII 2.5" SSD (Swappable SSD Tray)
Front I/O	
DC IN	1x DC-IN with D38999 connector
X1	1x DVI with D38999 connector
X2	1x USB3.0 with D38999 pin type connector
X3	1x USB3.0 with D38999 pin type connector
X4	1x GbE LAN + 2x RS232 with D38999 connector
X5	1x 3G-SDI output with BNC connector
LED	1x SSD/HDD LED indicator
Switch	1x IP65 power button with LED backlight
Rear I/O	
GND	1x GND screw
Applications	
Applications	Military IP65 GPU Rugged Mission MIL-STD 810 Computer is built to meet strict size, weight, and power (SWaP) requirements and to withstand harsh environments, including temperature extremes, shock/vibe, sand/dust, and salt/fog.
Operation System	
OS Support	Windows 10 / 11 64Bit, Linux by option
Mechanical & Environment	
Chassis	Aluminum Alloy, Corrosion design
Finish	Anodic aluminum oxide
Cooling	Natural Passive Convection/Conduction. No Moving Parts
Ingress Protection	IP65
Power Requirements	Power Supply, 18-36V DC In
Dimension (W x D x H)	250 x 350 x 105mm (9.84" x 13.78" x 4.14")
Operating Temp.	-20 to 60°C
Storage Temp.	-40 to 85°C
Relative Humidity	5% to 95%, non-condensing

*All specifications and photos are subject to change without notice.

1-2. Dimensions(2D)



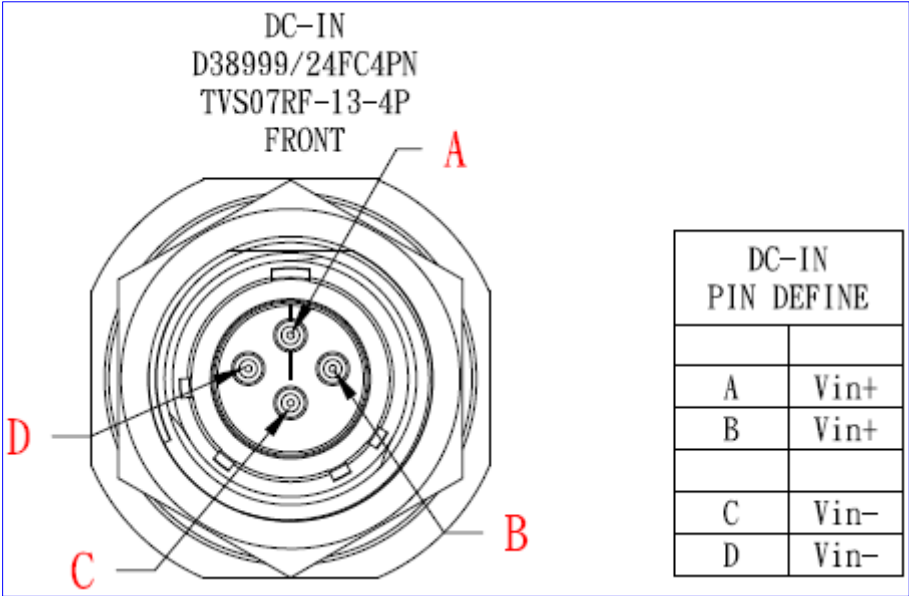
1-3. Panel Component



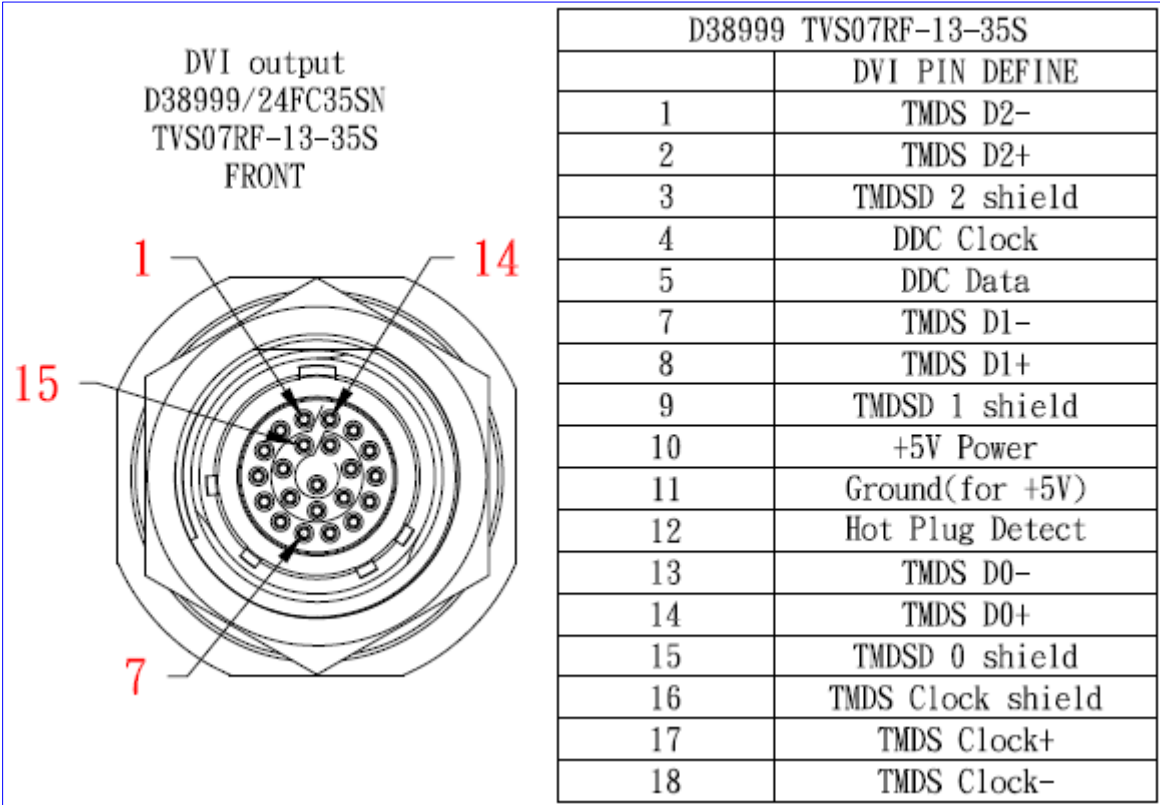
CHAPTER 2: JUMPERS AND CONNECTORS LOCATIONS

2.1. D38999 Connect Pin Definitions

DC-In



X1: 1x DVI

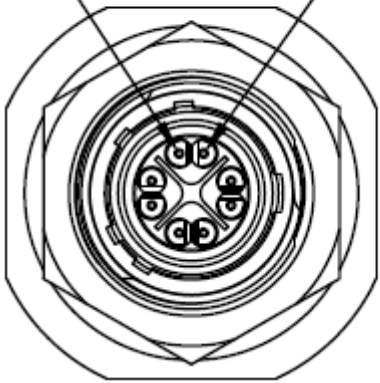


AV600-RH-AB User's Manual

Revision Date: Mar. 13. 2025

X2 & X3: Each 1x USB3.0 with Pin type D38999 connector

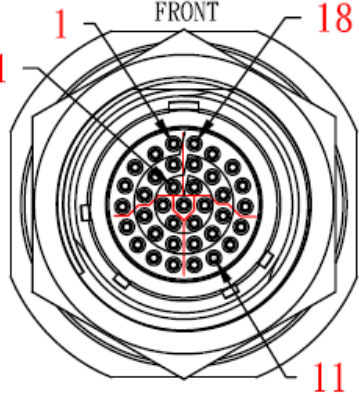
USB3. 2GEN1
TV07UCOM-USB3SF11S
FRONT



D38999 TV07UCOM-USB3SF11S		
		USB3
RED	1	VCC
BLACK	2	GND
WHITE	3	D-
GREEN	4	D+
BLUE	5	StdA_SSRX-
YELLOW	6	StdA_SSRX+
PURPLE	7	StdA_SSTX-
ORANGE	8	StdA_SSTX+

X4: 1x GbE LAN + 2x COM(RS232)

LAN / 2xCOM
TVS07RF-15-35S
FRONT



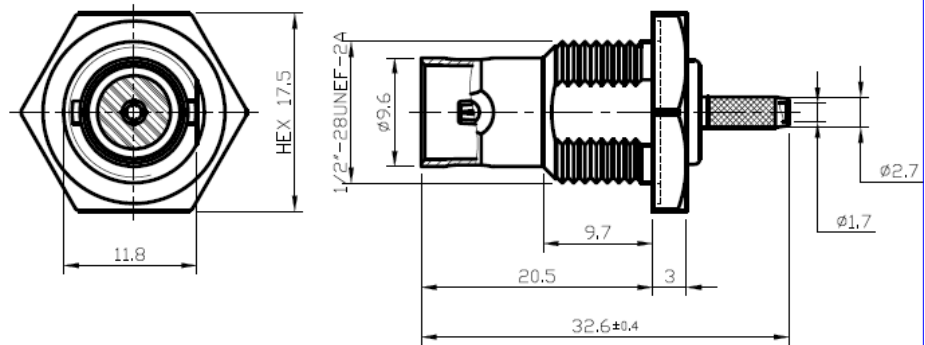
RJ45		
CON2		
White/orange	1	TX1+
Orange	19	TX1-
White/Green	2	TX2+
Green	20	TX2-
Blue	4	TX3-
White/Blue	3	TX3+
White/Brown	5	TX4+
Brown	21	TX4-

RS232 PORT_1		
CON2		RS232
Black	6	DCD_1
Brown	7	RXD_1
Red	8	TXD_1
Orange	9	DTR_1
Yellow	22	GND_1
Green	23	DSR_1
Blue	24	RTS_1
Purple	32	CTS_1
Gray	33	RI_1

RS232 PORT_2		
CON2		RS232
Black	10	DCD_2
Brown	11	RXD_2
Red	12	TXD_2
Orange	13	DTR_2
Yellow	25	GND_2
Green	26	DSR_2
Blue	27	RTS_2
Purple	34	CTS_2
Gray	35	RI_2

X5: 1x 3G-SDI with BNC connector

NOMINAL IMPEDANCE : 75Ω
FREQUENCY RANGE : DC ~3GHz
VSWR : 1.5MAX
PACKING : 100PCS/BAG



CHAPTER 3: BIOS SETUP ITEMS

This chapter provides users with detailed descriptions on how to set up a basic system configuration through the AMI BIOS setup utility.

3.1 INTRODUCTION

The following section describes the BIOS setup program. The BIOS setup program can be used to view and change the BIOS settings for the module. Only experienced users should change the default BIOS setting.

3.2 BIOS SETUP

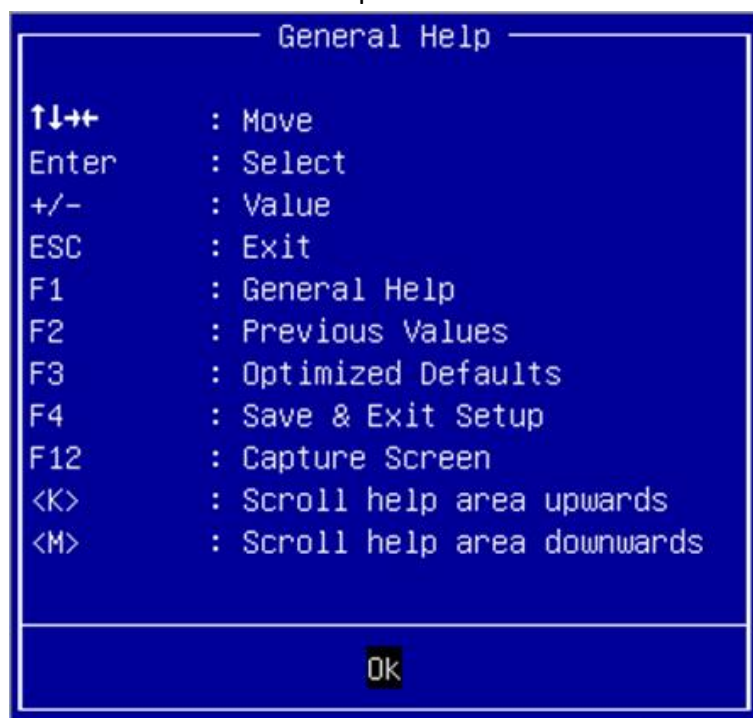
Power on the computer and the system will start POST (Power on Self Test) process. When the message below appears on the screen, press <Delete> or <ESC> key will enter BIOS setup screen.

Press <ESC> or <Delete> to enter SETUP

If the message disappears before responding and still wish to enter Setup, please restart the system by turning it OFF and On or pressing the RESET button. It can be also restarted by pressing <Ctrl>, <Alt>, and <Delete> keys on keyboard simultaneously.

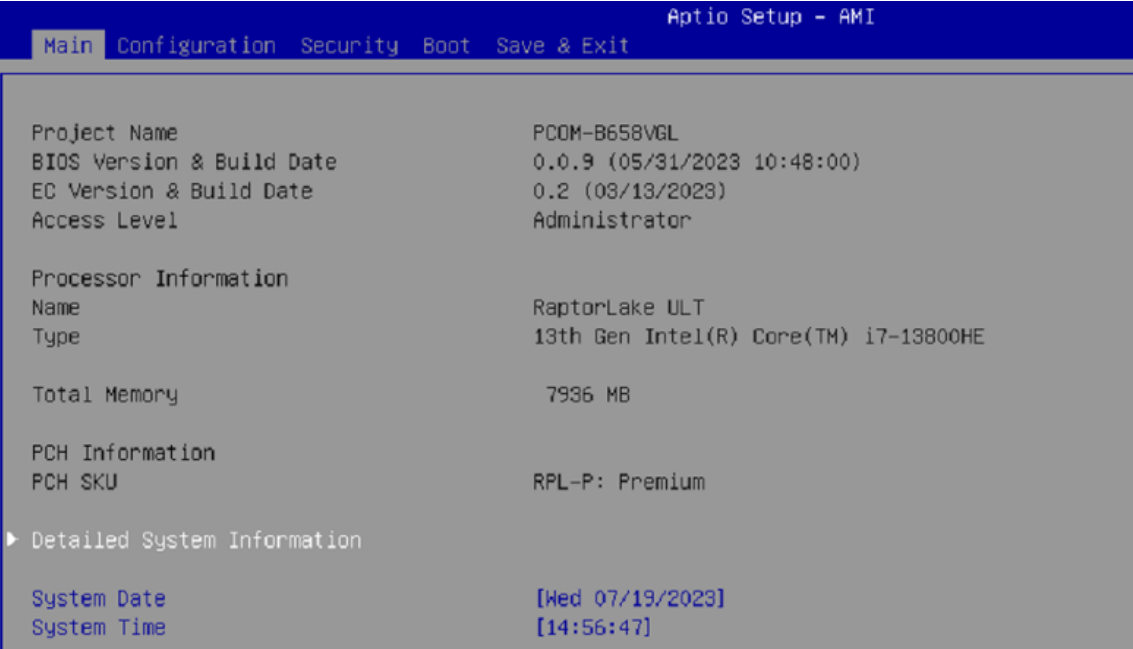
Press <F1> to Run General Help or Resume

The BIOS setup program provides a General Help screen. The menu can be easily called up from any menu by pressing <F1>. The Help screen lists all the possible keys to use and the selections for the highlighted item. Press <Esc> to exit the Help Screen.



3.2.1 MAIN

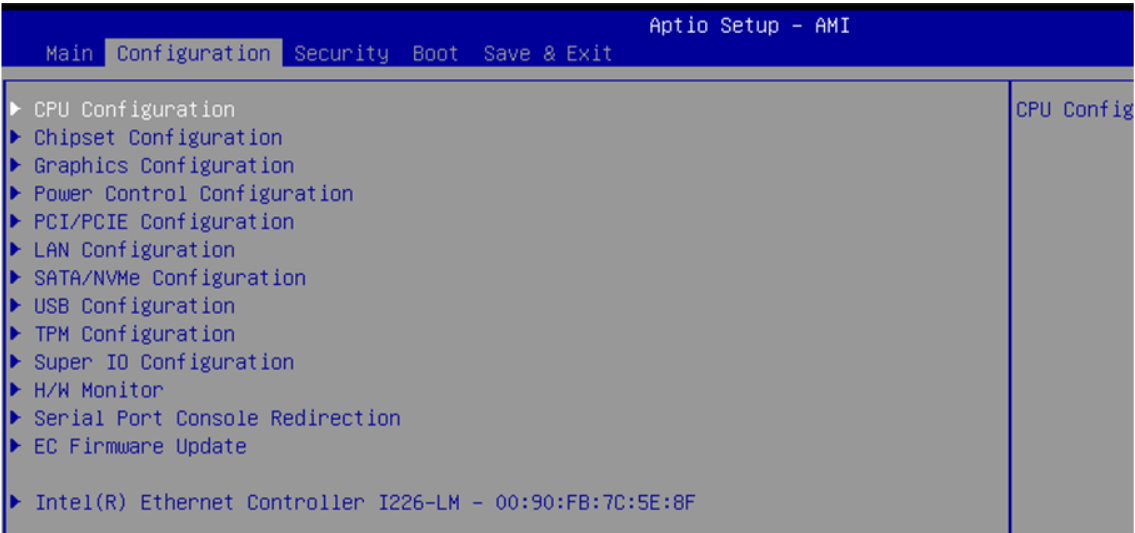
Use this menu for basic system configurations, such as time, date etc.



Feature	Description	Options
Detailed System Information		
System Date	The date format is <Day>, <Month> <Date> <Year>. Use [+] or [-] to configure system Date.	
System Time	The time format is <Hour> <Minute> <Second>. Use [+] or [-] to configure system Time.	

3.2.2 CONFIGURATION

Use this menu to set up the items of special enhanced features.



3.2.2.1 CPU CONFIGURATION

CPU Configuration Parameters.

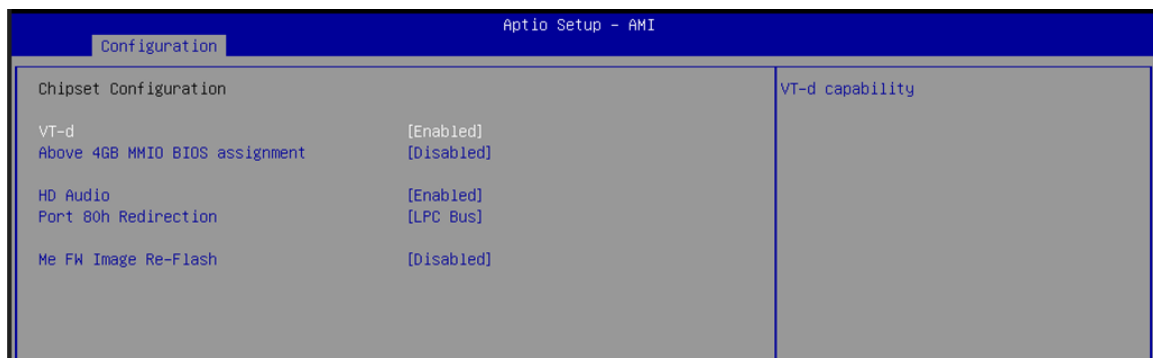
Configuration		Aptio Setup - AMI
CPU Configuration		
Type	13th Gen Intel(R) Core(TM) i7-13800HE	Number of P-cores to enable in each processor package. Note: Number of Cores and E-cores are looked at together. When both are {0,0}, Pcode will enable all cores.
ID	0xB06A2	
Speed	2500 MHz	
L1 Data Cache	48 KB x 6	
L1 Instruction Cache	32 KB x 6	
L2 Cache	1280 KB x 6	
L3 Cache	24 MB	
VMX	Supported	
SMX/TXT	Supported	
Active Performance-cores	[All]	
Active Efficient-cores	[All]	++: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit F8: PCI List F12: Capture Screen ESC: Exit
Hyper-Threading	[Enabled]	
Boot performance mode	[Turbo Performance]	
Intel (VMX) Virtualization Technology	[Enabled]	
Intel(R) SpeedStep(tm)	[Enabled]	
Intel(R) Speed Shift Technology	[Enabled]	
Turbo Mode	[Enabled]	
C states	[Enabled]	
Enhanced C-states	[Enabled]	
C-State Auto Demotion	[C1]	
C-State Un-demotion	[C1]	
Package C-State Demotion	[Enabled]	
Package C-State Un-demotion	[Enabled]	
CState Pre-Wake	[Enabled]	
ID MWAIT Redirection	[Disabled]	
Package C State Limit	[Auto]	

Feature	Description	Options
Active Performance-cores	Number of P-cores to enable in each processor package. Note: Number of Cores and E-cores are looked at together. When both are {0,0}, Pcode will enable cores	★All, 5, 4, 3, 2, 1
Active Efficient-cores	Number of E-cores to enable in each processor package. Note: Number of Cores and E-cores are looked at together. When both are {0,0}, Pcode will enable cores	★All, 7, 6, 5, 4, 3, 2, 1, 0
Hyper-Threading	Enabled or Disabled Hyper-Threading Technology.	★Enabled, Disabled
Boot performance mode	Select the performance state that the BIOS will set starting from reset vector	Max Battery, Max Non-Turbo Performance ★Turbo Performance,
Intel (VMX) Virtualization Technology	When enabled, a VMM can utilize the additional hardware capabilities provided by Vander pool Technology.	Disabled, ★Enabled
Intel® Speed Step™	Allows more than two frequency ranges to be supported.	Disabled, ★Enabled
Intel® Speed Shift Technology	Enable/Disable Intel® Speed Shift Technology support. Enabling will expose the CPPC v2 interface to allow for hardware controlled P-states	Disabled, ★Enabled
Turbo Mode	Enable/Disable processor Turbo Mode (requires Intel Speed Step or Intel Speed Shift to be available and enabled)	Disabled, ★Enabled
C states	Enable/disable CPU Power Management. Allows CPU to go to C states It's not 100% utilized	Disabled, ★Enabled

Enhanced C-States	Enable/disable C1E. When enabled, CPU will switch to minimum speed when all cores enter C-State.	Disabled, ★Enabled
C-State Auto Demotion	Configure C-State Auto Demotion	Disable, C1 ,C3 , ★C1 and C3
C-State Un-demotion	Configure C-State Un-demotion	Disable, C1 ,C3 , ★C1 and C3
Package C State Demotion	Package C-State Demotion	★Disabled, Enabled
Package C State Un-demotion	Package C-State Un-demotion	★Disabled, Enabled
CState Pre-Wake	Disable – Sets bit 30 of POWER_CTL MSR(0x1FC) to 1 to disable the Cstate Pre-Wake	Disabled, ★Enabled
IO MWAIT Redirection	When set, will map IO_read instructions sent to IO registers PMG_IO_BASE_ADDRBASE+offset to MWAIT(offset)	★Disabled, Enabled
Package C State Limit	Maximum Package C State Limit Setting. Cpu Default: Leaves to Factory default value. Auto: Initializes to deepest available Package C States Limit	★Auto,C0/C1,C2,C3,C6 ,C7, C7S,C8,C9,C10,Cpu Default,

3.2.2.2 CHIPSET CONFIGURATION

Configuration Chipset feature.



Feature	Description	Options
VT-d	VT-d Capability	★Enabled ,Disabled
Above 4GB MMIO BIOS assignment	Enable/Disable above 4GB MemoryMappedIO BIOS assignment This is enabled automatically when Aperture Size is set to 2048MB	★Disabled, Enabled
HD Audio	Control Detection of the HD-Audio device. Disabled= HAD will be unconditionally disabled Enabled= HAD will be unconditionally enabled.	★Enabled ,Disabled
Port 80h Redirection	Control where the Port 80h cycles are sent	★LPC Bus, PCIE Bus
Me FW Image Re-Flash	Enable/Disable Me FW Image Re-Flash function	★Disabled, Enabled

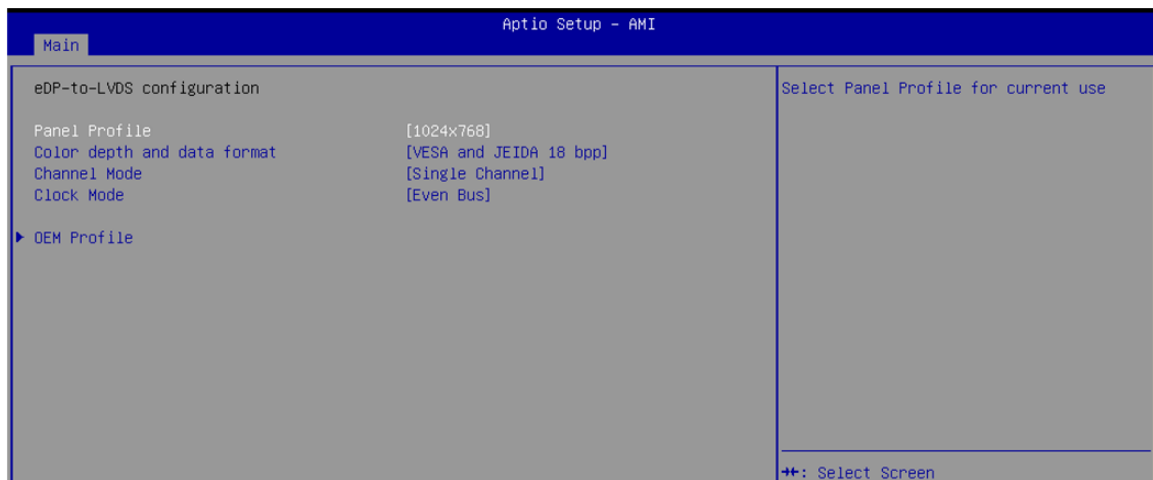
3.2.2.3 GRAPHICS CONFIGURATION

Configuration Graphics Settings.

Feature	Description	Options
Primary Display	Select which of IGFX/PEG/PCI Graphics device should be Primary Display Or select SG for Switchable Gfx.	★Auto, IGFX, PEG, PCI
Internal Graphics	Keep IGFX enable based on the setup options.	★Auto, Disable, Enable
DVMT Pre-Allocated	Select DVMT 5.0 Pre-Allocated (Fixed) Graphics Memory size used by the Internal Graphics Device.	0M,32M,64M,4M,8M,12M,16M,20M,24M,28M,32M/F7,36M,40M,44M,48M,52M,56M,★60M
DVMT Total Gfx Mem	Select DVMT5.0 Total Graphic Memory size used by the Internal Graphics Device	★256M, 128M, MAX

3.2.2.4 EDP-TO-LVDS CONFIGURATION

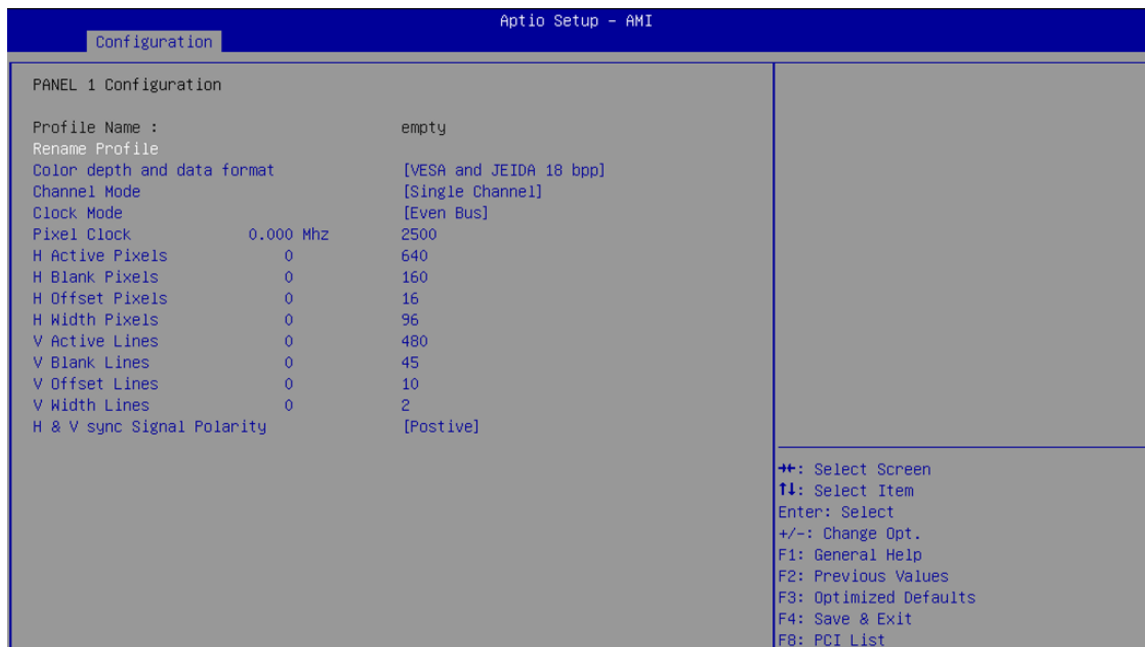
eDP-to-LVDS.



Feature	Description	Options
Panel Profile	Select Panel Profile for current use.	★1024x768,640x480,800x480,800x600,1280x800,1280x1024,1366x768,1440x900,1920x1080,OEM Profile
Color depth and data format	Select Color depth and data format	★VESA and JEIDA 18 bpp, VESA 24 bpp, JEIDA 24 bpp
Channel Mode	Select LVDS Channel Mode	★Single Channel, Dual Channel
Clock Mode	Select clock output for LVDS.	★Even Bus, Odd Bus, Both Buses

3.2.2.5 OEM PROFILE

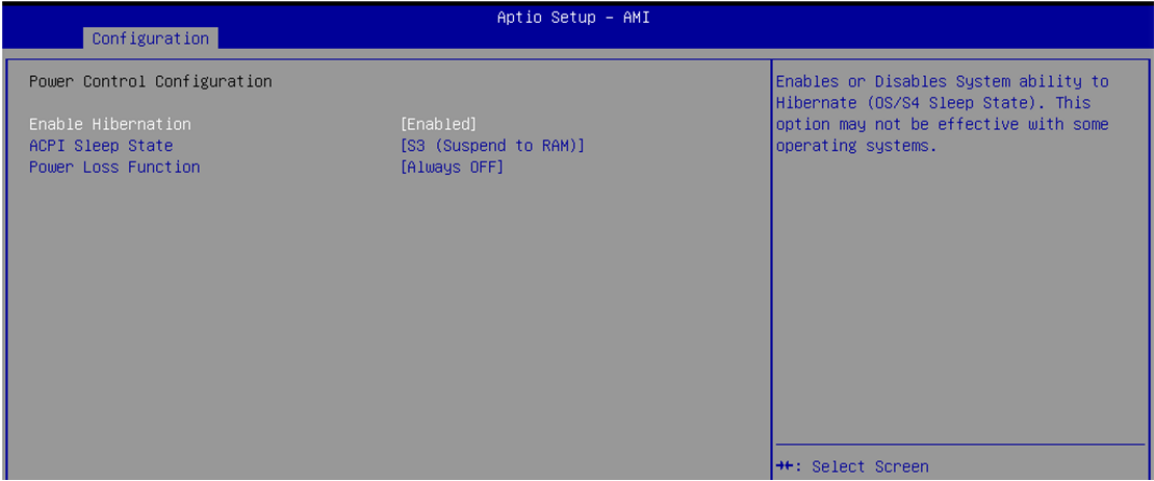
PANEL 1 Configuration.



Feature	Description	Options
Color depth and data format	Select Color depth and data format	★VESA and JEIDA 18 bpp, VESA 24 bpp, JEIDA 24 bpp
Channel Mode	Select LVDS Channel Mode	★Single Channel, Dual Channel
Clock Mode	Select clock output for LVDS.	★Even Bus, Odd Bus, Both Buses
Pixel Clock	Pixel Clock(10Khz)	★2500
H Active Pixels	H Active Pixels (Pixel)	★640
H Blank Pixels	H Blank Pixels (Pixel)	★160
H Offset Pixels	H Offset Pixels (Pixel)	★16
H Width Pixels	H Width Pixels (Pixel)	★96
V Active Lines	V Active Lines (Line)	★480
V Blank Lines	V Blank Lines (Line)	★45
V Offset Lines	V Offset Lines (Line)	★10
V Width Lines	V Width Lines (Line)	★2
H&V sync Signal Polarity	Flag: 0x1E Signal Polarity is Postive 0x18 Signal Polarity is Non-Postive	★Postive, Non-Postive

3.2.2.6 POWER CONTROL CONFIGURATION

System Power Control Configuration Parameters.



Feature	Description	Options
Enable Hibernation	Enables or Disables System ability to Hibernate (OS/S4 Sleep State). This option may be not effective with some operating system	Disabled, ★Enabled
ACPI Sleep State	Select the highest ACPI sleep state the system will enter when the SUSPEND button is pressed.	Suspend Disabled , ★S3 (Suspend to RAM)
Power Loss Function	Control SIO Power Loss Function. ON is always ON, OFF is always OFF, Last state will depends on last power state	★Always OFF, Always ON, Last State,

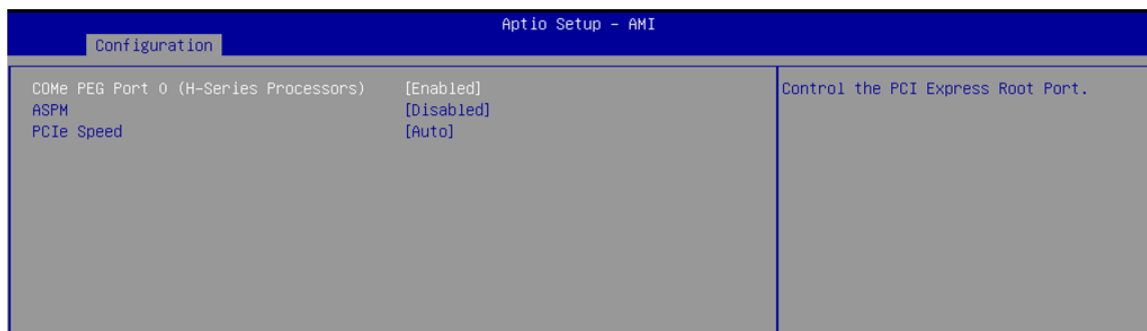
3.2.2.7 PCI/PCIE CONFIGURATION

PCI, PCI-X and PCI Express Settings.



3.2.2.8 COME PEG PORT 0, 8, 12

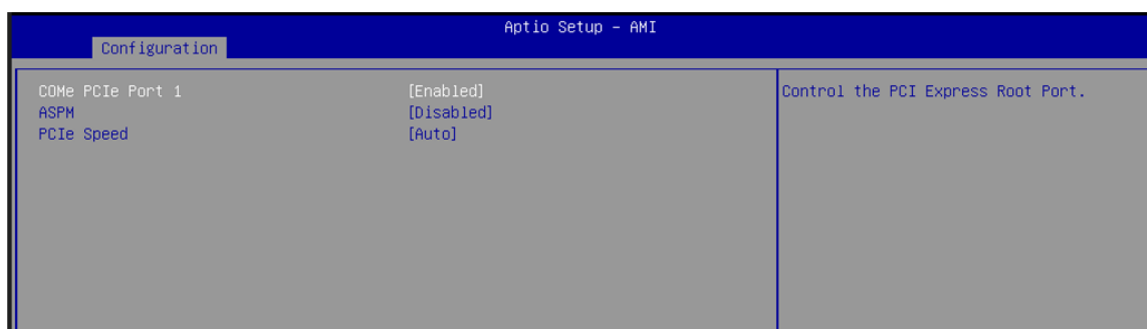
PCI Express Root Port Settings.



Feature	Description	Options
COME PEG Port 0, 8, 12	Control the PCI Express Root Port.	Disabled, ★Enabled
ASPM	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO - BIOS auto configure DISABLE – Disables ASPM	★Disabled, L0s, L1, L0sL1, Auto
PCIe Speed	Configure PCIe Speed	★Auto, Gen1, Gen2, Gen3

3.2.2.9 PCH PCI EXPRESS ROOT PORT 1~5

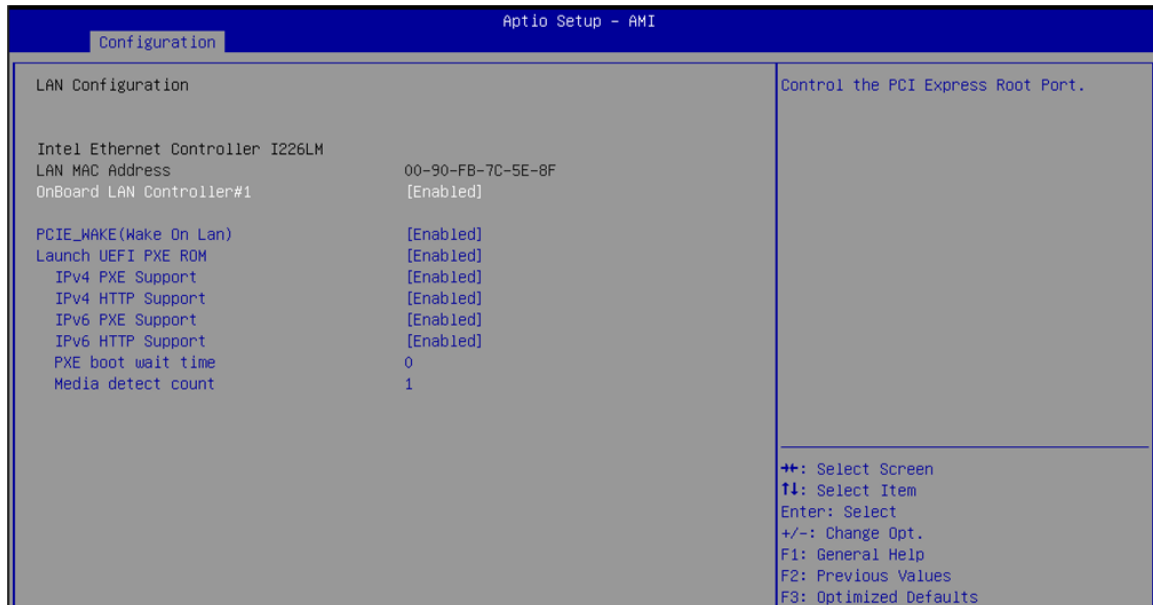
PCI Express Root Port Settings.



Feature	Description	Options
COME PCIe Port 1~5	Control the PCI Express Root Port.	Disabled, ★Enabled
ASPM	Set the ASPM Level: Force L0s – Force all links to L0s State AUTO - BIOS auto configure DISABLE – Disables ASPM	★Disabled, L0s, L1, L0sL1, Auto
PCIe Speed	Configure PCIe Speed	★Auto, Gen1, Gen2, Gen3

3.2.2.10 LAN CONFIGURATION

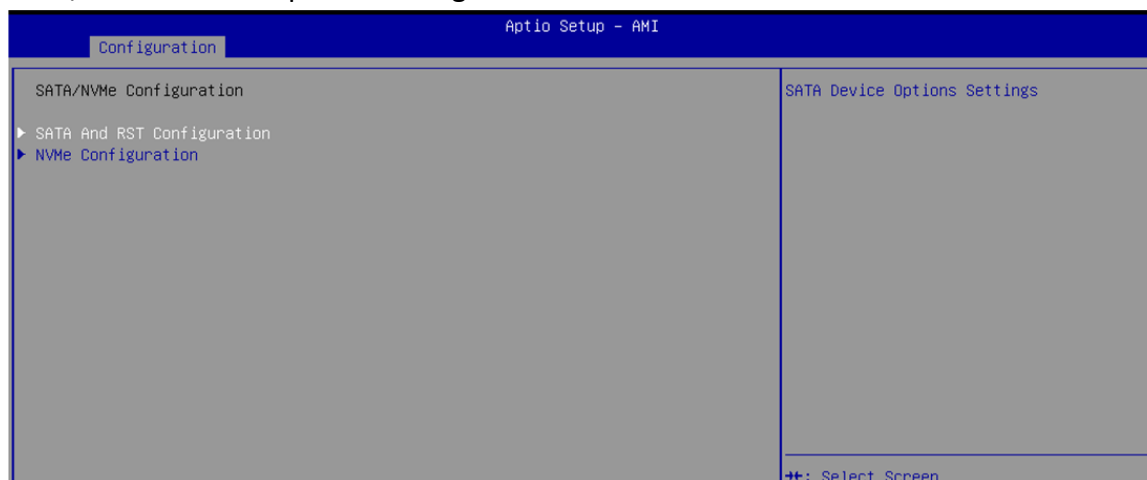
Configuration On Board LAN Device.



Feature	Description	Options
Onboard LAN Controller #1	Enable/Disable onboard NIC	★Enabled , Disabled
PCIE_WAKE (Wake on LAN)	Control PCIE wake# pin for Wake On Lan function	★Enabled , Disabled
Launch UEFI PXE ROM	Enable/Disable UEFI Network Stack	★Disabled, Enabled
Launch UEFI PXE ROM[Enable]		
Ipv4 PXE Support	Enable/Disable Ipv4 PXE boot support.	Disabled, ★Enabled
Ipv4 HTTP Support	Enable/Disable Ipv4 HTTP boot support. If disable, IPv4 HTTP boot support will not be available.	Disabled, ★Enabled
Ipv6 PXE Support	Enable/Disable Ipv6 PXE boot support. If disable, IPv6 PXE boot support will not be available.	Disabled, ★Enabled
Ipv6 HTTP Support	Enable/Disable Ipv6 HTTP boot support. If disable, IPv6 HTTP boot support will not be available.	Disabled, ★Enabled
IPSEC Certificate	Support to Enable/Disable IPSEC certificate for Ikev	Disabled, ★Enabled
PXE boot wait time	Wait time in seconds to press ESC key to abort the PXE boot. Use either +/- or numeric keys to set the values	★0
Media detect count	Number of times the presence of media will be checked. Use either +/- or numeric keys to set the values.	★1

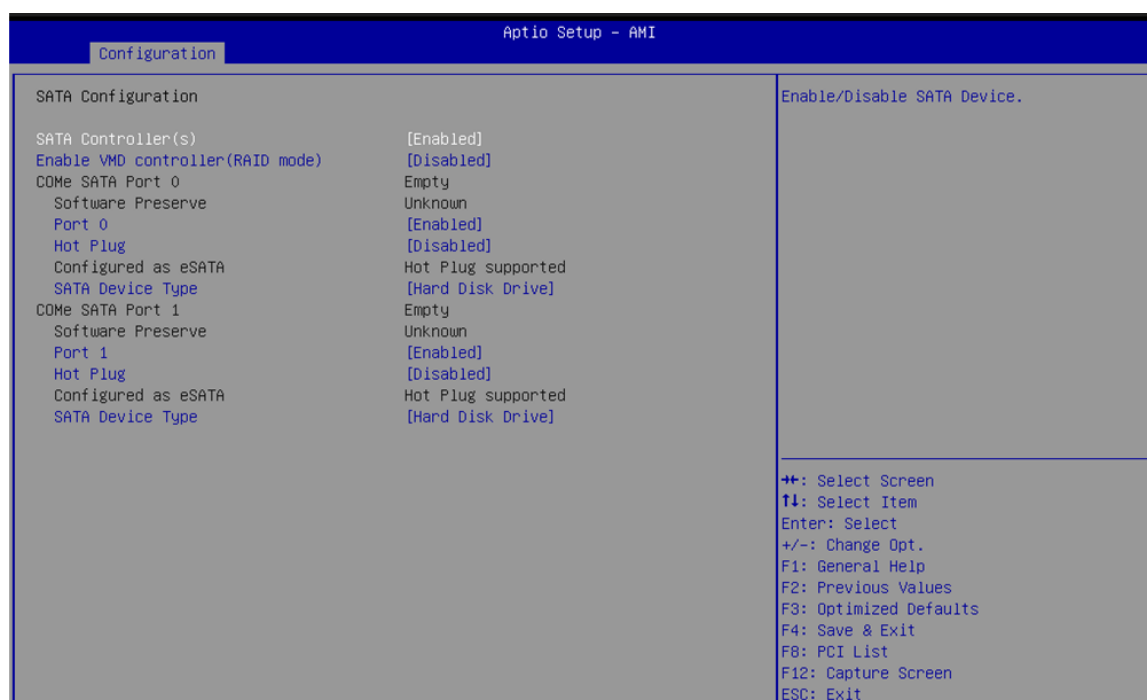
3.2.2.11 SATA CONFIGURATION

SATA/NVMe Device Options Settings.



Feature	Description	Options
SATA And RST Configuration	SATA Device Options Settings	
NVMe Configuration	NVMe Device Options Settings	

3.2.2.12 SATA AND RST CONFIGURATION

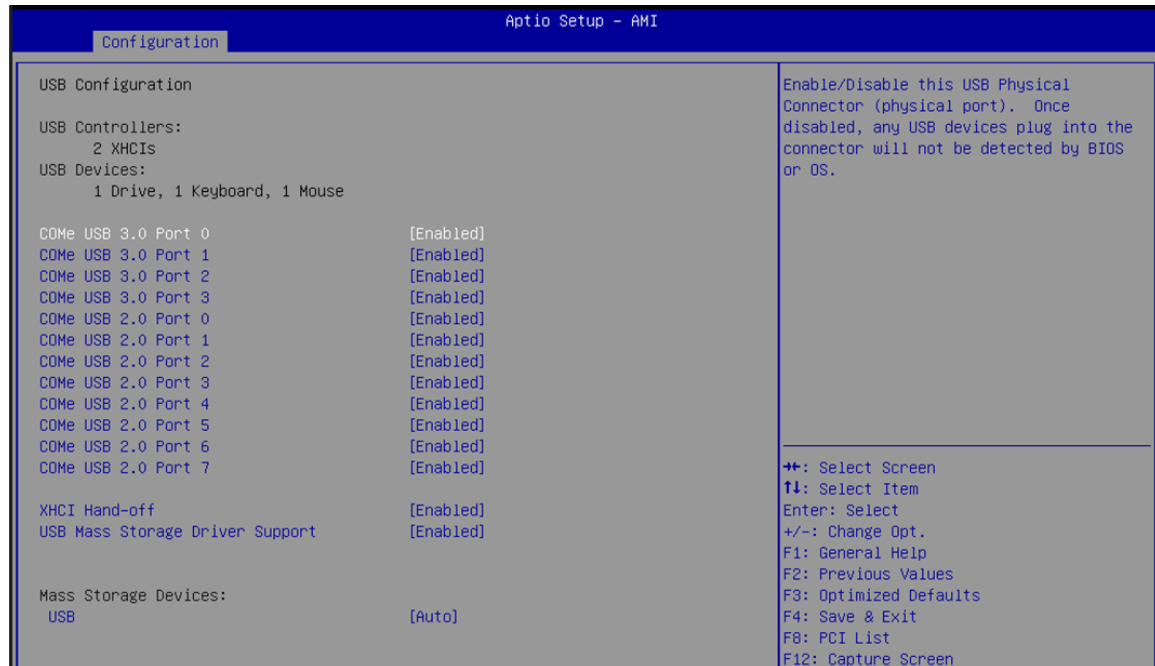


Feature	Description	Options
SATA Controller(s)	Enable/Disable the SATA Device.	★Enabled , Disabled
Enable VMD controller(RAID Mode)	Enable/Disable to VMD controller	★Disabled, Enabled
COMe SATA Port 0~1		
Port 0~1	Enable or Disable SATA Port	★Enabled ,Disabled

Hot Plug	Designates this port as Hot Pluggable	★Disabled, Enabled
SATA Device Type	Identify the SATA port is connected to Solid State Drive or Hard Disk Drive	★Hard Disk Drive,Solid State Drive

3.2.2.13 USB CONFIGURATION

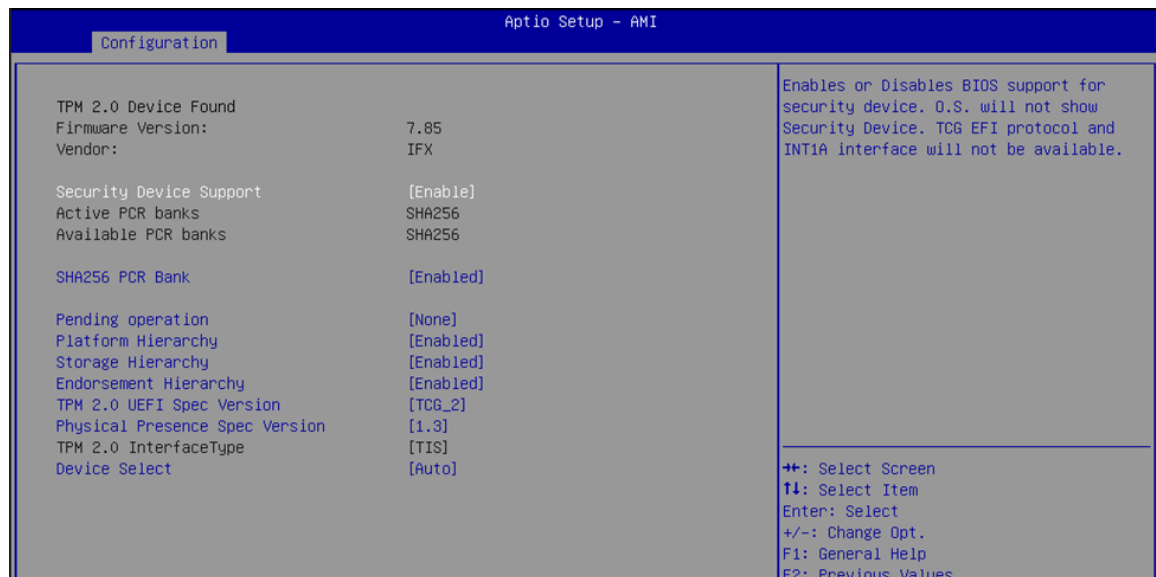
USB Configuration Parameters.



Feature	Description	Options
COMe USB 3.0 Port #0~7	Enable/Disable this USB Physical Connector (physical port). Once disable, any USB devices plug into the connector will not be detected by BIOS or OS	★Enabled ,Disabled
XHCI Hand-off	This is a workaround for OSES without XHCI hand-off support. The XHCI ownership change should be claimed by XHCI driver	★Enabled ,Disabled
USB Mass Storage Driver Support	Enable/Disable USB Mass Storage Driver Support	★Enabled ,Disabled
USB	Mass storage device emulation type. 'AUTO' enumerates devices according to their media format. Optical drives are emulated as 'CDROM', drives with no media will be emulated according to a drive type	★Auto, Floppy, Forced FDD, Hard Disk, CD-ROM

3.2.2.14 TPM CONFIGURATIN

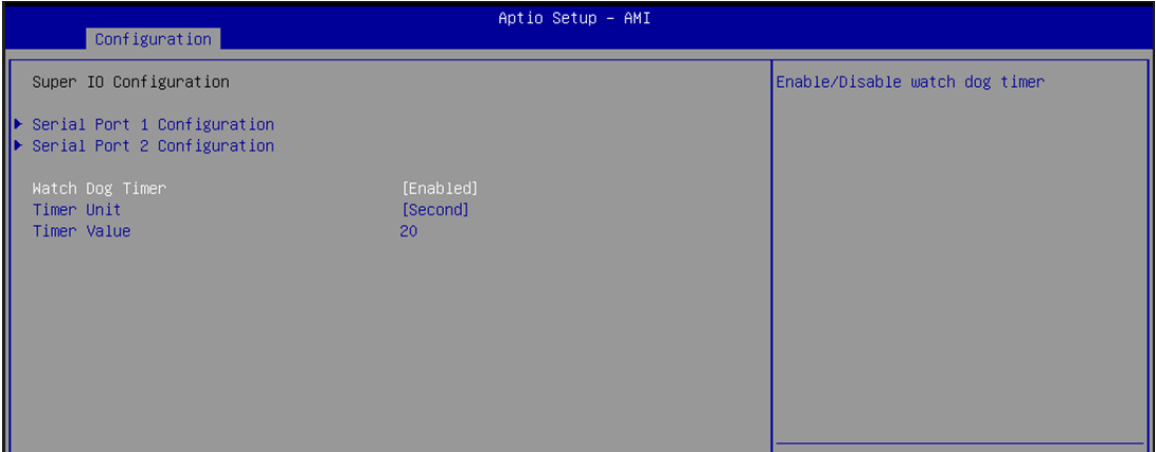
Trust Computing Settings.



Feature	Description	Options
Security Device Support	Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A Interface will not be available.	Disabled, ★Enabled
SHA256 PCR Bank	Enables or Disables SHA256 PCR Bank	Disabled, ★Enabled
Pending operation	Schedule an Operation for the Security Device. Note: Your Computer will reboot during restart in order to change State of Security Device	★None, TPM Clear
Platform Hierarchy	Enables or Disables Platform Hierarchy	Disabled, ★Enabled
Storage Hierarchy	Enables or Disables Storage Hierarchy	Disabled, ★Enabled
Endorsement Hierarchy	Enables or Disables Endorsement Hierarchy	Disabled, ★Enabled
TPM 2.0 UEFI Spec Version	Select the TCG2 Spec Version Support, TCG_1_2: the Compatible mode for Win8/Win10 TCG_2: Support new TCG2 protocol and event format for Win10 or later	TCG_1_2, ★TCG_2
Physical Presence Spec Version	Select to Tell O.S. to support PPI Spec Version 1.2 or 1.3. Note some HCK tests might not support 1.3.	1.2, ★1.3
Device Select	TPM 1.2 will restrict support to TPM 1.2 devices, TPM 2.0 will restrict support to TPM 2.0 devices, Auto will support both with the default set to TPM2.0 devices if not found, TPM 1.2 devices will be enumerated	TPM1.2, TPM2.0, ★Auto

3.2.2.15 SUPER IO CONFIGURATION

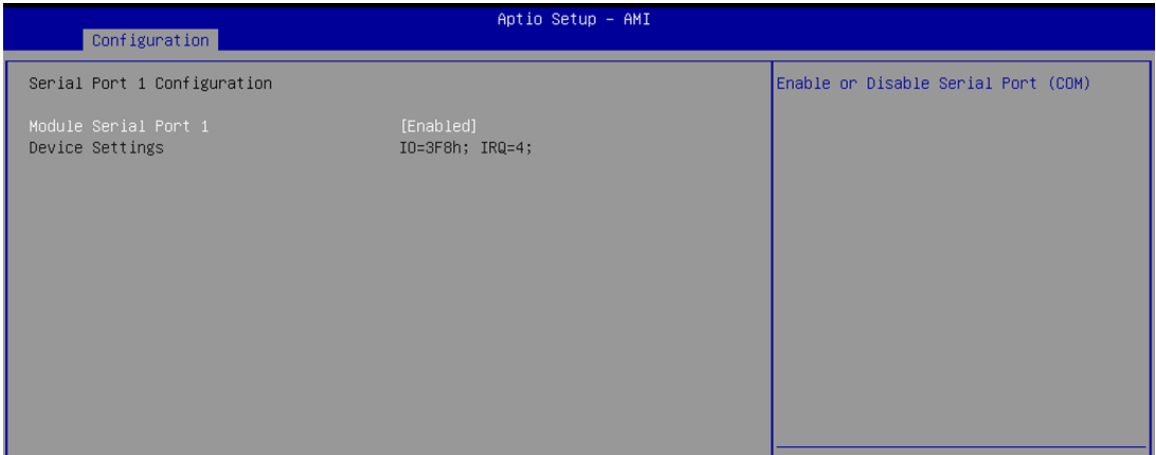
System Super IO Chip Parameters.



Feature	Description	Options
Watch Dog Timer	Enable/Disable Watch Dog Timer	★Disabled, Enabled
Watch Dog Timer [Enable]		
Timer Unit	Select Timer count unit of WDT	★Second, Minute
Timer value	Set WDT Timer value seconds/minutes	★20

3.2.2.16 SERIAL PORT 1 CONFIGURATION

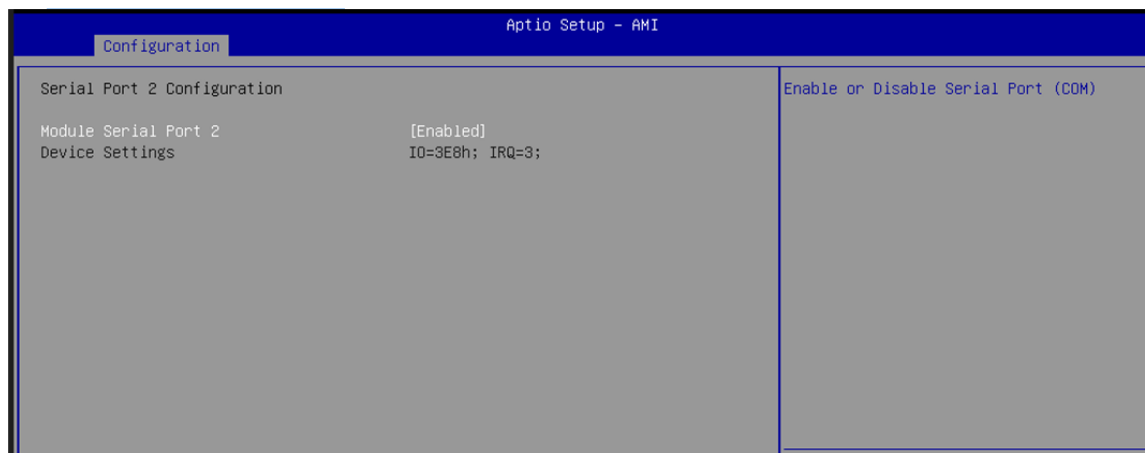
Set Parameters of Serial Port 1.



Feature	Description	Options
Module Serial Port 1	Enable or Disable Serial Port (COM)	★Enabled, Disabled

3.2.2.17 SERIAL PORT 2 CONFIGURATION

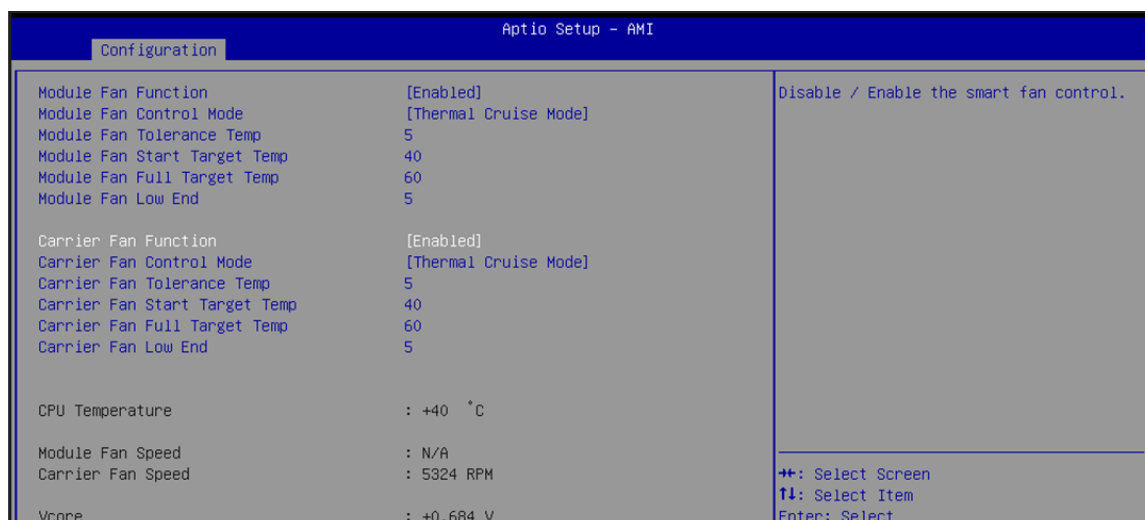
Set Parameters of Serial Port 2.



Feature	Description	Options
Serial Port 2	Enable or Disable Serial Port (COM)	★Enabled, Disabled

3.2.2.18 H/W MONITOR

Monitor hardware status.

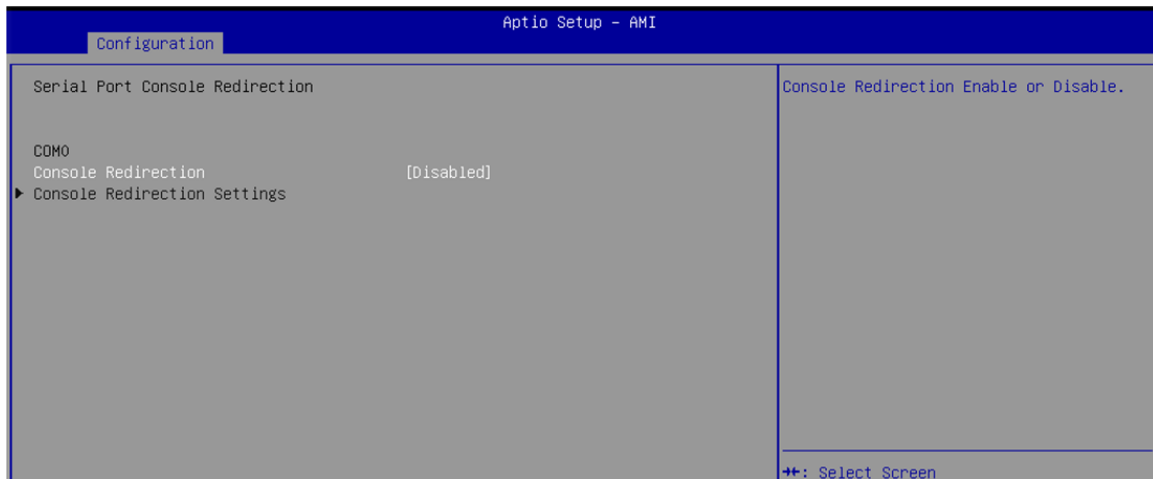


Feature	Description	Options
Module Fan Function	Enable/Disable the smart fan control	★Disabled, Enabled
Module Fan Function [Enable]		
Module Fan Control Mode	Smart Fan Mode Select	★Thermal Cruise Mode, Fan Control Mode
Module Fan Tolerance Temp	In Thermal Cruise Mode: Tolerance of Target Temperature	★5
Module Fan Start	In Thermal Cruise Mode: Start Temperature	★40

Target Temp		
Module Fan Full Target Temp	In Thermal Cruise Mode: Full Speed Temperature	★60
Module Fan Low End	In Thermal Cruise Mode: Low end of fan speed (0~100%)	★5
Carrier Fan Function	Enable/Disable the smart fan control	★Disabled, Enabled
Carrier Fan Function [Enable]		
Carrier Fan Control Mode	Smart Fan Mode Select	★Thermal Cruise Mode, Fan Control Mode
Carrier Fan Tolerance Temp	In Thermal Cruise Mode: Tolerance of Target Temperature	★5
Carrier Fan Start Target Temp	In Thermal Cruise Mode: Start Temperature	★40
Carrier Fan Full Target Temp	In Thermal Cruise Mode: Full Speed Temperature	★60
Carrier Fan Low End	In Thermal Cruise Mode: Low end of fan speed (0~100%)	★5

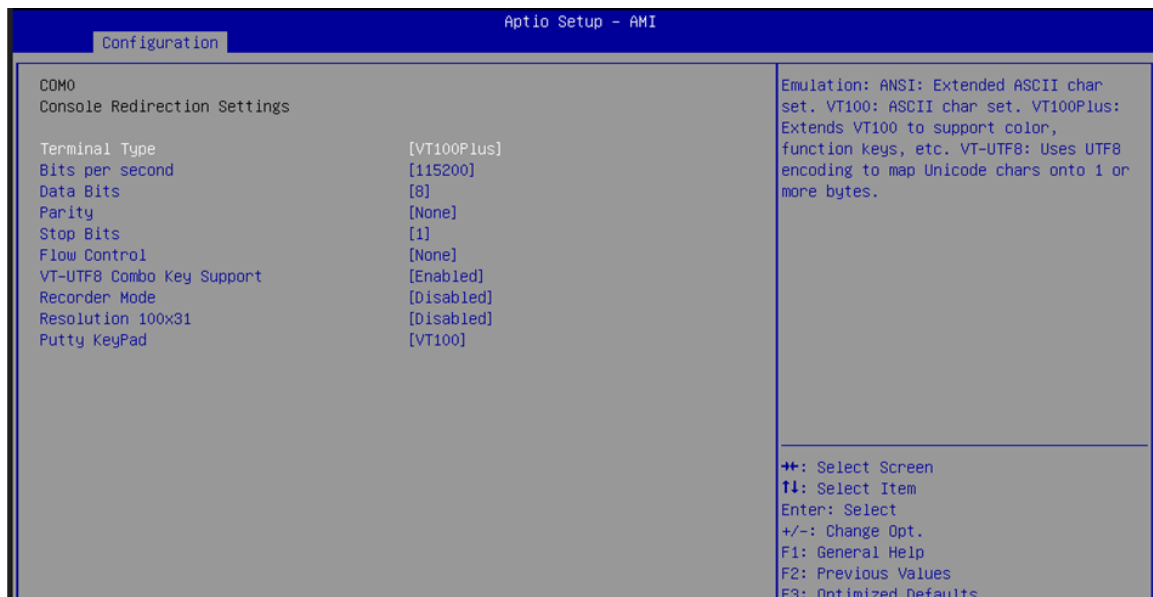
3.2.2.19 SERIAL PORT CONSOLE REDIRECTION

Serial Port Console Redirections.



Feature	Description	Options
Console Redirection	Console Redirection Enable or Disable	★Disabled, Enabled

3.2.2.20 COMO CONSOLE REDIRECTION SETTINGS

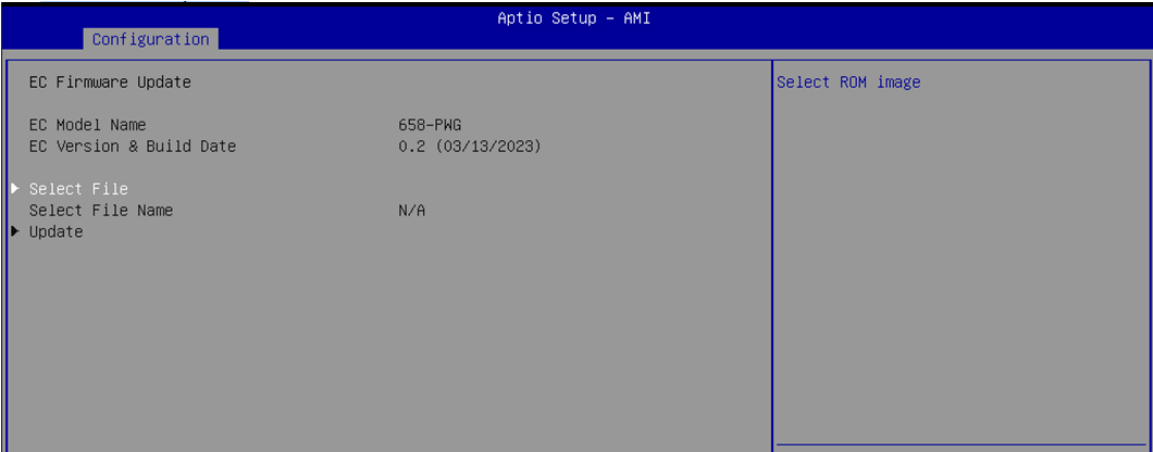


Feature	Description	Options
Terminal Type	Emulation: ANSI: Extended ASCII char set. VT100: ASCII char set. VT100+: Extends VT100 to support color , function keys, etc. VT-UTF8: Uses UTF8 encoding to map Unicode chars onto 1 or more bytes.	VT100,★VT100 Plus, VT-UTF8, ANSI
Bits per second	Select Serial port transmission speed. The speed must be matched on other side. Long or noisy lines may require lower speeds.	★115200, 9600, 19200, 38400, 57600
Data bits	Data bits	★8, 7
Parity	A parity bit can be sent with the data bits to detect some transmission errors. Even: parity bit is 0 if the num of 1's in the data bits is even. Odd: parity bit is 0 if num of 1's in the data bits is odd. Mark: parity bit is always 1. Space parity bit is always 0. Mark and Space Parity do not allow for error detection. They can be used as an additional data bit.	★None, Even, Odd, Mark, Space
Stop Bits	Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit.	★1,2
Flow Control	Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signal.	★None, Hardware RTS/CTS
VT-UTFB Combo Key Support	Enable VT-UTF8 Combination Key Support for ANSI/VT100 terminals	★Enabled, Disabled
Recorder	With this mode enabled only text will be sent. This is to	★Disabled,

Mode	capture Terminal data.	Enabled
Resolution 100x31	Enables or disables extended terminal resolution	★Disabled, Enabled
Putty KeyPad	Select FunctionKey and KeyPad on Putty	★VT100, LINUX,XTERMR 6, SCO,ESCN,VT4 00

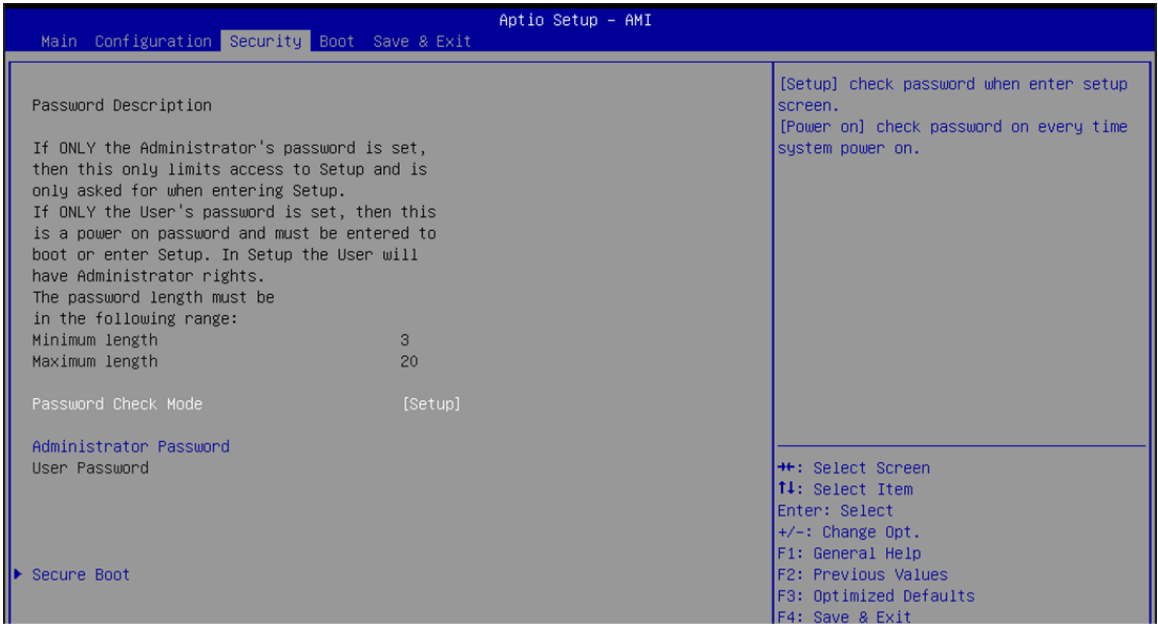
3.2.2.21 EC FIRMWARE UPDATE

EC Firmware Update.



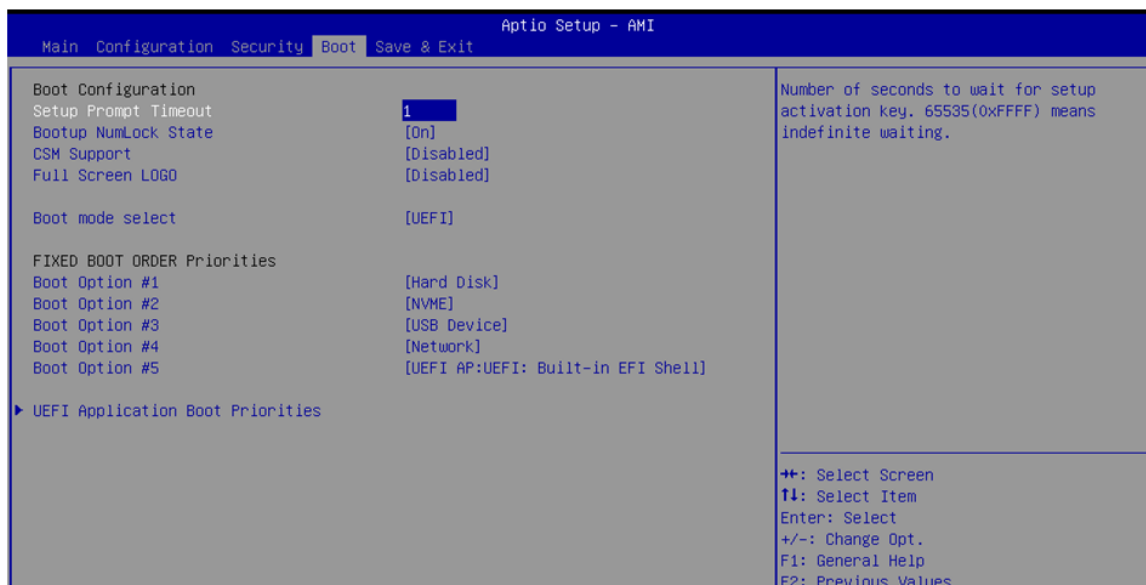
Feature	Description	Options
Select File	Select ROM image	

3.2.3 SECURITY



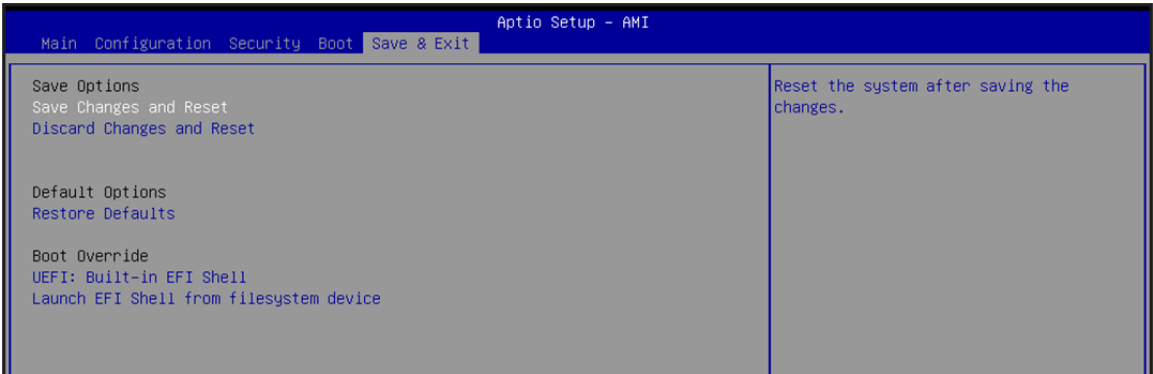
Feature	Description	Options
Password Check Mode	[Setup] check password when enter setup screen. [Power on] check password on every time system power on.	★Setup, Power on
Administrator Password	Set Administrator Password	

3.2.4 BOOT



Feature	Description	Options
Setup Prompt Timeout	Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.	★1
Bootup NumLock State	Select the keyboard NumLock state	★On, Off
CSM Support	Enable/Disable CSM support	★Disabled
Full Screen LOGO	Enables or disables Quiet Boot option and Full screen Logo.	★Disabled, Enabled
Boot mode select	Select boot mode LEGACY/UEFI	★UEFI ,Legacy
Boot Option #1~5	Sets the system boot order	★Hard Disk, NVME, USB Device, Network, UEFI AP: UEFI: Built-in EFI Shell, Disabled
UEFI Application Boot Priorities	Specifies the Boot Device Priority sequence from available UEFI Application	

3.2.5 SAVE & EXIT



Feature	Description	Options
Save Changes and Reset	Reset the system after saving the changes.	
Discard Changes and Reset	Reset system setup without saving any changes.	
Restore Defaults	Restore/Load Default values for all the setup options.	
UEFI: Built-in EFI Shell	Reset the system after saving the changes. (Boot option filter: UEFI only)	
Launch EFI Shell from filesystem device	Attempts to Launch EFI Shell application (Shell.efi) from one of the available filesystem devices.	

3.3 BIOS / EC UPDATE

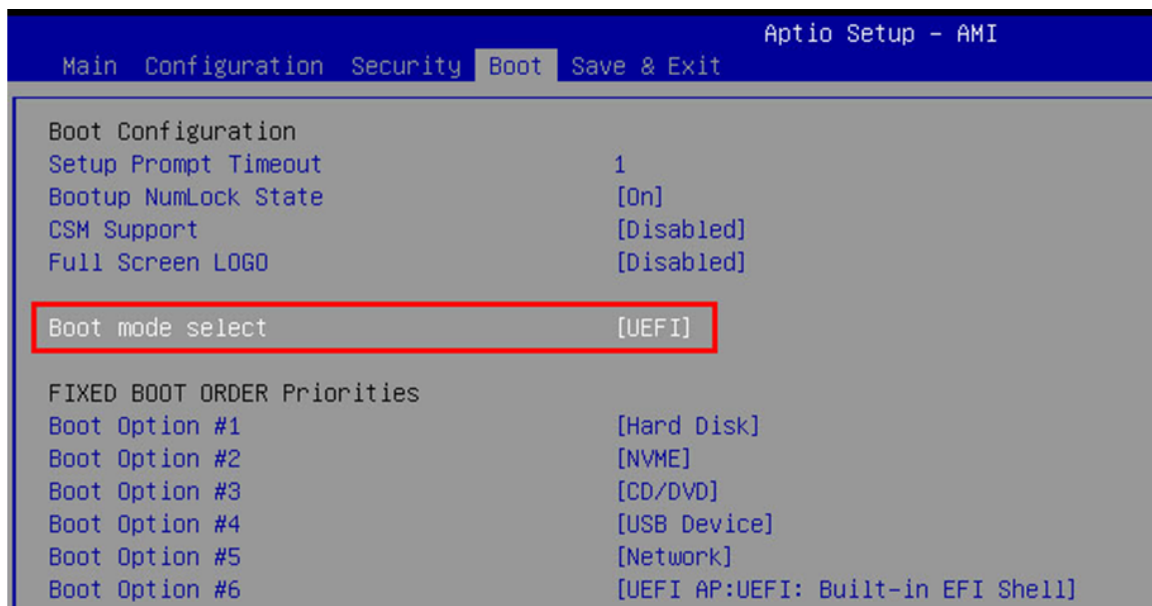
PCOM-B658VGL only support BIOS/EC update under UEFI shell environment, refer the following step, please.

3.3.1 BIOS UPDATE

Step 1. Unzip update file to the USB DOK (USB DOK must be FAT or FAT32 format)

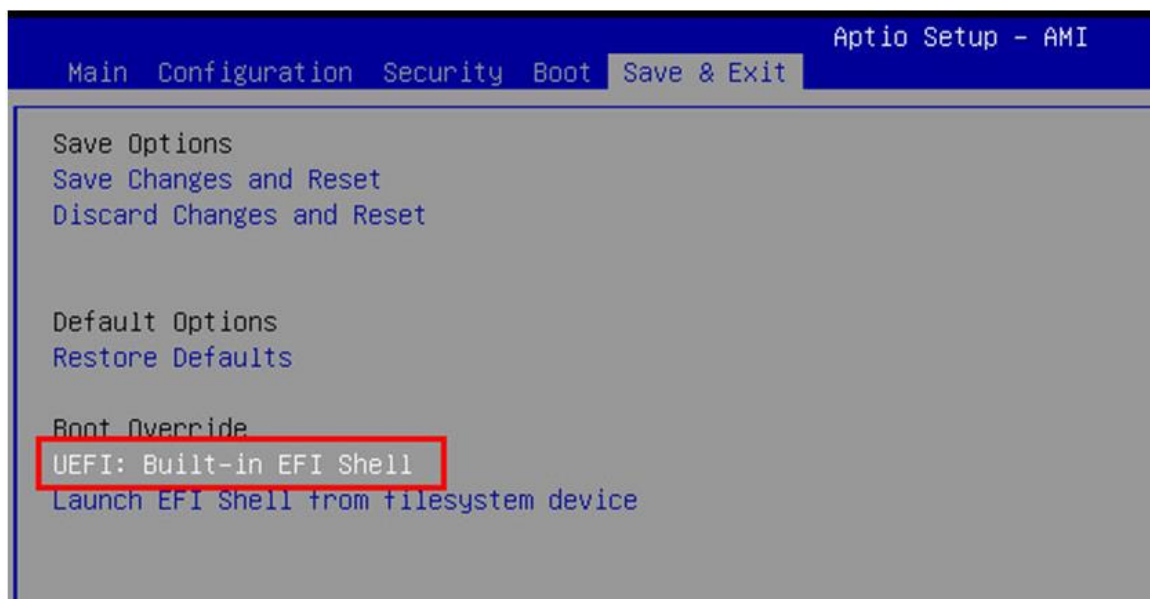
Step 2. Make sure "Boot mode select" item is "UEFI" in the BIOS "Boot" page

Shown as below picture.



Step 3. Plug the USB DOK on the target system and select "Built-in EFI Shell" in the BIOS "Save & Exit" page

Shown as below picture



Step 4. Under the UEFI shell, change prompt to your USB DOK, the below example is "fs0: "

Step 5. Then change the folder with updated file and use command: "update " and press enter

```
FS0:\> cd Update_FPT_PCOM-B657VGL_0_0_16
FS0:\Update_FPT_PCOM-B657VGL_0_0_16> Update.efi_
```

Step 6. The updating process will start and show the updating progress

Step 7. Please power off and restart the system once updating finished

```
EDK II
UEFI v2.70 (American Megatrends, 0x00050013)
Mapping table
  FS0: Alias(s):HD0r0b::BLK1:
        PciRoot(0x0)/Pci(0x14,0x0)/USB(0x11,0x0)/HD(1,MBR,0x6A4499BF,0x800,0x1
D6B800)
  BLK0: Alias(s):
        PciRoot(0x0)/Pci(0x14,0x0)/USB(0x11,0x0)
Intel (R) Flash Programming Tool Version: 15.0.30.1776
Copyright (C) 2005 - 2021, Intel Corporation. All rights reserved.

Reading HSFSTS register... Flash Descriptor: Valid

--- Flash Devices Found ---
ID:0xC22019   Size: 32768KB (262144Kb)

GbE Region does not exist.

- Erasing Flash Block [0x2000000] - 100 percent complete.
- Programming Flash [0x2000000] 32768KB of 32768KB - 100 percent complete.
RESULT: The data is identical.32768KB of 32768KB - 100 percent complete.

FPT Operation Successful.

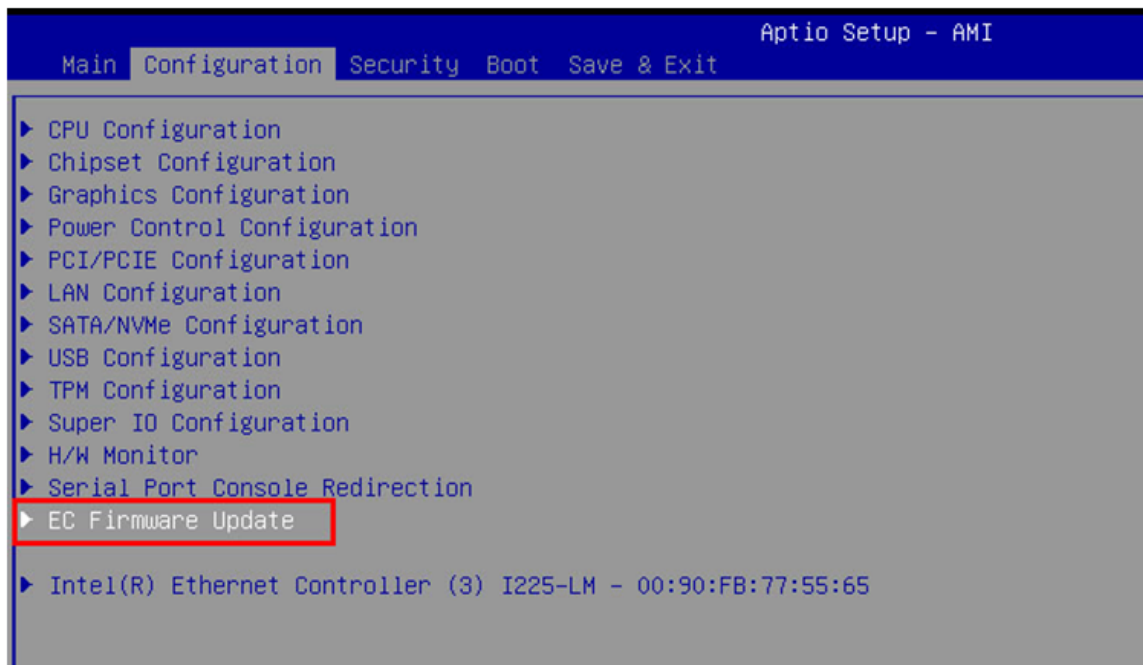
FS0:\Update_FPT_PCOM-B657VGL_0_0_16> _
```

(BIOS updating finished)

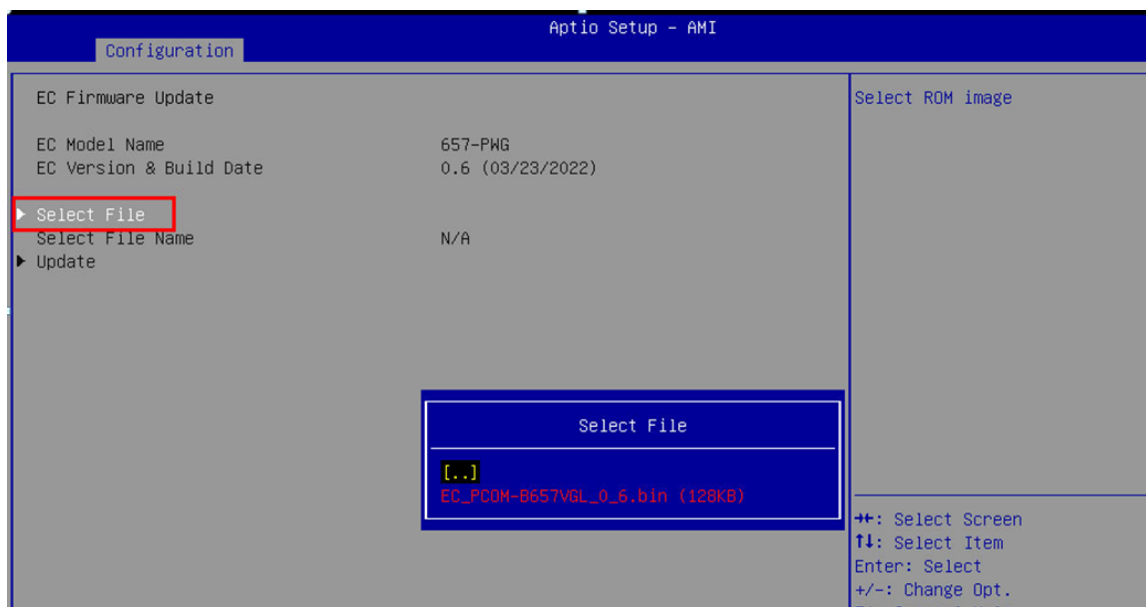
3.3.2 EC UPDATE

Step 1. Unzip EC binary file to the USB DOK (USB DOK must be FAT or FAT32 format)

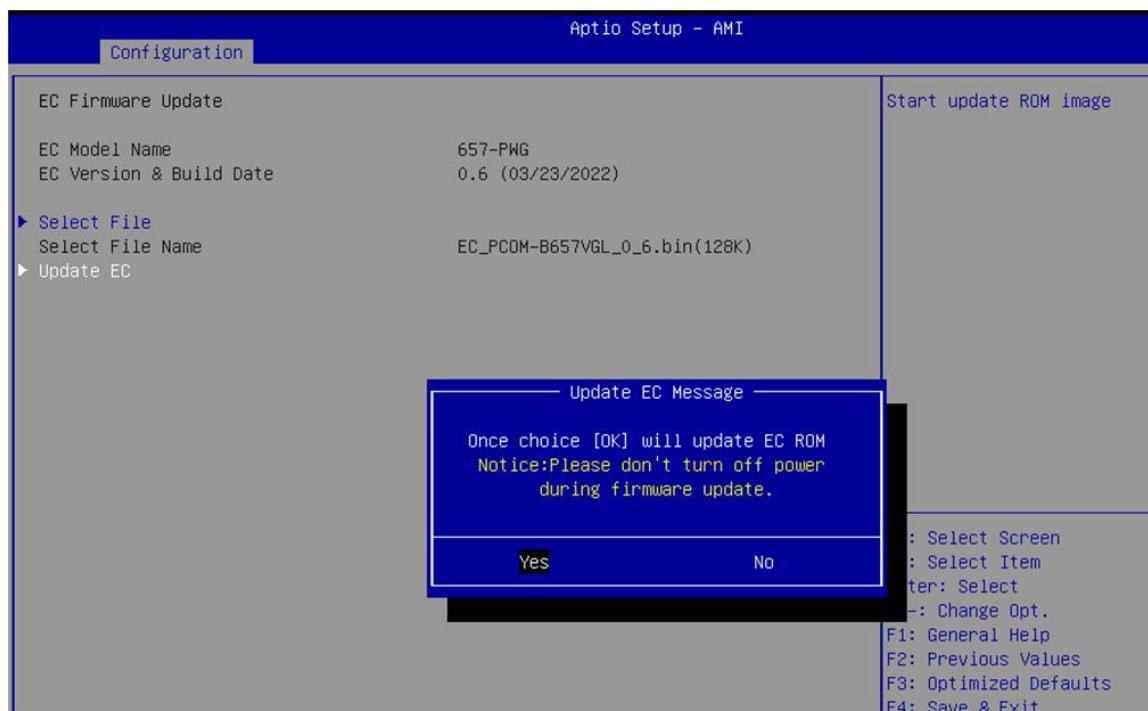
Step 2. Select "EC Firmware Update" item in BIOS setup menu



Step 3. Select EC binary file by option item shown as below



Step 4. Select “Yes” to start EC update (Please don’t turn off power during firmware update)



Step 5. Turn off power to make system into G3 status once updating finished, then power on the system

